

**WEST****Freeform Search****Database:**

US Patents Full-Text Database  
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**Term:****Display:** 20 **Documents in Display Format:** CIT **Starting with Number** 1**Generate:**  Hit List  Hit Count  Image       **Search History****Today's Date:** 8/16/2001

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(4217660  '4959863'  ✓ '5068878')[ABPN1,NRPN,PN,TBAN,WKU]	10	<u>L8</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	16 and (interleav\$3 or permut\$5 or de?interleav\$3 ) near3(array or matrix or matric\$3 or block\$4 or group\$3) near3 (column or row) same random\$3 same (series or sequen\$5 or serial\$3)	4	<u>L7</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	15 and (interleav\$3 or permut\$5 or de?interleav\$3 or arrang\$5 or re?arrang\$5) near3(array or matrix or matric\$3 or block\$4 or group\$3) near3 (column or row) same random\$3 same (series or sequen\$5 or serial\$3)	83	<u>L6</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	14 and (interleav\$3 or permut\$5 or de?interleav\$3 or arrang\$5 or re?arrang\$5) near3(array or matrix or matric\$3 or block\$4 or group\$3) near3 (column or row) same random\$3	627	<u>L5</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(interleav\$3 or permut\$5 or de?interleav\$3 or arrang\$5 or re?arrang\$5) near3(array or matrix or matric\$3 or block\$4 or group\$3)	170051	<u>L4</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(5204981  '6151001')[ABPN1,NRPN,PN,TBAN,WKU]	5	<u>L3</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(EP 998046A  m ventors '5204981')[ABPN1,NRPN,PN,TBAN,WKU]	4	<u>L2</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(interleav\$3 or permut\$5 or de?interleav\$3 or arrang\$5 or re?arrang\$5) same (array or matrix or matric\$3 or block\$4 or group\$3) same (column or row) same random\$3 same time near (series or sequen\$5 or serial\$3)	5	<u>L1</u>

**WEST** **Generate Collection**

L7: Entry 3 of 4

File: USPT

Sep 25, 1990

DOCUMENT-IDENTIFIER: US 4959863 A  
TITLE: Secret speech equipment

## DEPR:

The decimated signal sequence (signal vector)  $Y_{\sup{16}}$  is permuted by a multiplication by the permutation matrix  $[T]$  of  $8 \times 8$ . In this case, the row element of the permutation matrix is 0 or 1 (the sum being 1), and element of this matrix is 0 or 1 (the sum being 1). The permutation matrix is a fixed permutation if constant with time, and a variable permutation if variable. In the scramble processing, the rows of this matrix are permuted at random, and the number of combinations is usually  $n!$  for an  $n \times n$  matrix.

## DEPR:

This translation is carried out in the TDM-FDM converter 51 shown in FIG. 5. A series of  $Y_{\sup{16}}$  ( $Z$ ) (which was permuted by the permutation matrix) is again split into sub-bands through the band splitting filters ( $H_{\sup{0}} \text{ to } H_{\sup{7}}$ ) 620 to 627. In this process, all the components of 4 kHz to 8 kHz are made zero.



**WEST****End of Result Set** **Generate Collection**

L7: Entry 4 of 4

File: EPAB

Aug 12, 1980

DOCUMENT-IDENTIFIER: US 4217660 A

TITLE: Method and apparatus for the coding and decoding of digital data

## FPAR:

Digital information is convolutionally encoded and stored in row by successive row sequence in a storage matrix. The interleaved data from the matrix is outputted in column by successive column order for transmission over a medium. The decoding process includes loading the encoded bit stream in column by successive column sequence in a decoder matrix. The stored data is then outputted in row by row sequence, thereby deinterleaving the bit stream. The original information bits are then regenerated by processing the bit stream through a convolutional decoder. The instant data processing system is capable of correcting for one out of four successive random errors and burst errors having a length of one-fourth of the entire message.



Refine Query:

(interleave or permute or deinterleave)<near> (matrix or array) <near>

Collections: Journals Conferences Standards

Your search matched 1 of 506922 documents.

1 are presented on this page, sorted by Score in descending order

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DOC  
TYPE

CNF



**A 2K/spl times/1K space switch ASIC for use in digital exchanges**

*Keshava Murthy, V.; Madhu Kumar, K.; Vani, M.B.; Jagadish Kumar, D.; Mohan, C.S.; Prasanna, B.S.*

VLSI Design, 1994., Proceedings of the Seventh International Conference on  
Published: 1994 , Page(s): 247 -250